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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sommer
Docket No.: INF-126
Serial No.: 10/753,407
Art Unit: 2822
Filed: January 9, 2004
Examiner: Ida M. Soward
For: Integrated Semiconductor Memory Circuit and Method of Manufacturing
the Same

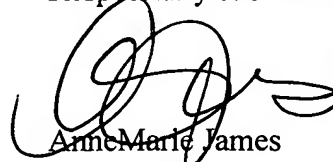
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Date of Deposit: August 18, 2005

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Certificate of Mailing via First Class Mail (1 page)
Amendment (9 pages)
Formal Drawings (3 pages), including 2 Replacement Sheets
Supplemental Declaration and Power of Attorney (3 pages)
Return Postcard

Respectfully submitted,


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